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24131 7590 03/29/2004
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| | |
|-------------------------|--------------------|
| LOREN DONALD PEARSON | (Depositor's name) |
| <i>Loren D. Pearson</i> | REG. NO. 42,987 |
| (Signature) | |
| June 29, 2004 | |
| (Date) | |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/715,072 | 11/17/2003 | Frank Klotz | WMP-IFT-648 | 3311 |

TITLE OF INVENTION: CIRCUIT CONFIGURATION FOR VOLTAGE STABILIZATION

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|----------------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 06/29/2004 |
| EXAMINER | ART UNIT | CLASS-SUBCLASS | | | |
| VU, BAO Q | 2838 | 323-274000 | | | |

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

Muenchen, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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LOREN DONALD PEARSON
REG. NO. 42,987

(Date)

6/29/04

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